

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10602	cache near3 miss	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:46
L2	8217	load adj instruction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:46
L3	194	first adj load adj instruction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:46
L4	152	second adj load adj instruction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:46
L5	116	3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:47
L6	9518	(abort or aborted or aborting or terminate or terminated or terminating or quash or quashed or quashing) near3 execut\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:48
L7	14	5 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:48
L8	24445974	@ad<"20030908"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:49
L9	12	7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:49
L10	11	1 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:51
L11	1699	711/118.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:51

L12	300	711/125.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:51
L13	1000	711/144.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:51
L14	1964	711/154.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:51
L15	825	711/156.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:51
L16	5217	11 or 12 or 13 or 14 or 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:52
L17	8	1 and 5 and 8 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:55
L18	1	6 and 17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 17:56
L19	421	711/118.ccls.	US-PGPUB	OR	ON	2005/09/30 17:56
L20	314	8 and 19	US-PGPUB	OR	ON	2005/09/30 17:57
L21	0	7 and 20	US-PGPUB	OR	ON	2005/09/30 17:57

Results for "(((cache <or> caching) <and> speculative loads)<in>metadata)"

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Select Article Information

- ☐ 1. **Performance evaluation of configurable hardware features on the AMD-K5**
Clark, M.; John, L.K.;
Computer Design, 1999. (ICCD '99) International Conference on
10-13 Oct. 1999 Page(s):102 - 107
Digital Object Identifier 10.1109/ICCD.1999.808413
[AbstractPlus](#) | Full Text: [PDF](#)(56 KB) IEEE CNF
- ☐ 2. **A study on the number of memory ports in multiple instruction issue machines**
Soo-Mook Moon; Ebcioglu, K.;
Microarchitecture, 1993. Proceedings of the 26th Annual International Symposium on
1-3 Dec. 1993 Page(s):49 - 58
Digital Object Identifier 10.1109/MICRO.1993.282757
[AbstractPlus](#) | Full Text: [PDF](#)(864 KB) IEEE CNF



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IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
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